

**AMENDMENTS TO THE SPECIFICATION**

Please insert the following paragraph at page 1, line 3:

IDC-A1,AMD

**CROSS-REFERENCE TO RELATED APPLICATIONS**

The present application is a continuation of PCT application PCT/CA02/01366 filed on September 10, 2002 designating the United States of America and currently pending, which also claims priority of US provisional application number 60/317,969, filed on September 10, 2001.

Please replace the paragraph at page 8, line 14 with the following amended paragraphs:

IDC-A2,AMD

According to a fourth broad aspect of the present invention, there is provided a circuit for trimming a functional resistor, the circuit comprising: ~~a thermally isolated micro-platform on a substrate;~~ a plurality of functional resistors ~~spaced apart on the thermally isolated micro-platform~~ thermally isolated on a substrate; and trimming circuitry for subjecting a portion of the ~~thermally isolated micro-platform~~ substrate to heat pulses such that a resistance value of one of said plurality of functional resistors is trimmed while a resistance value of remaining ones of said plurality of functional resistors remains substantially untrimmed.

Preferably, the circuit comprises a thermally-isolated micro-platform on the substrate and the plurality of functional resistors are placed on the thermally-isolated micro-platform.

Please replace the paragraph at page <sup>8</sup>, line 23 with the following amended paragraphs:

RP  
7/14/06

According to a fifth broad aspect of the present invention, there is provided a circuit for trimming circuit elements, the circuit comprising: ~~at least one thermally isolated micro-platform on a substrate;~~ at least two resistive elements with non-zero temperature induced drift ~~on said at least one thermally isolated micro-platform~~ thermally isolated on a substrate, such that said at least two resistive

IDC-A3,AMD,M